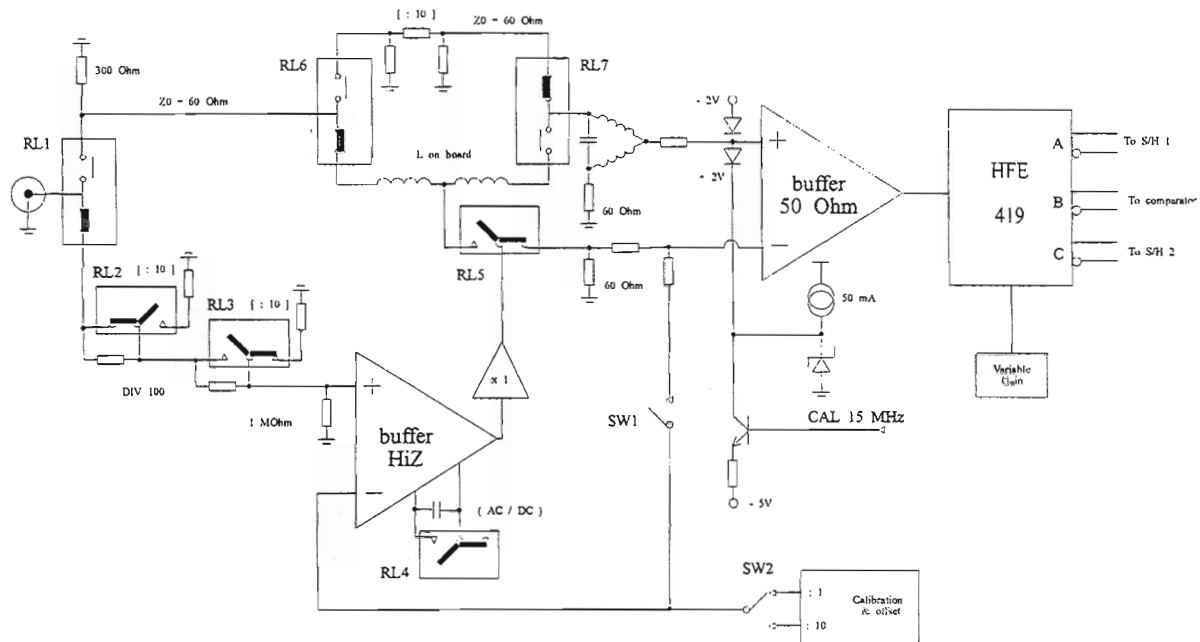


4.2.2.1 Block Diagram

4.2.2.1.a Power off State : 1 M Ω input

- Relay RL1 ($_50\Omega$) selects the input between the HiZ (1 M Ω) and the 50 Ω path. A diode circuit senses the temperature of the 300 Ω termination resistor (in parallel with 60 Ω to have $Z_{in} = 50\Omega$) and sets the $_OVL$ bit low if overheating is detected.
- The 50 Ω path is then disconnected by the hardware and the signal will be in the 1 Mohm input.
- Relay RL2 ($_10HZA$) selects the input between divide-by-10 or direct for the signal in the **HiZ** path.
- Relay RL3 ($_10HZA$) selects the input between divide-by-100 or divide-by-10 for the signal. in the **HiZ** path.
- Relay RL4 ($_DC$) sets the AC/DC coupling in HiZ.
- Relay RL5 ($_HZ$) enables the **HiZ output** (selection between 1M Ω output & offset for 50 Ω buffer).
- Relay RL6($_10HFA$) selects the input between divide-by-10 or direct for the signal. in the **50 Ohm** path.
- Relay RL7 ($_10HFB$) selects the output between
 - :1 in 50 Ω or HiZ (if RL5 ON)
 - :10 in 50 Ω .